

Application for a New Course v10.5

Course ID <i>Subject and Number</i>	ENGR 210
Date APPROVED	
Date DENIED	

I. Systems and Catalog Information Complete each item.

1	Course Developer	Darby Hewitt
2	Course Teacher	A member of the Engineering & Physics Department faculty
3	Course Title	Digital Logic
4	Course Abbreviation (if title is over 30 characters)	Digital Logic
5	College	College of Arts and Sciences
6	Department	Engineering and Physics
7	Number of Credit Hours	3
8	Is the course Fixed Credit or Variable Credit?	Fixed
9	Is the course repeatable?	No
10	Maximum number of times course may be repeated	0
11	Maximum number of hours credit	3
12	Explanation for variable credit	N/A
13a	Course contact hours - LEC	3
13b	Course contact hours - Lab	0
13c	Course contact hours - Practicum	0
13d	Course contact hours - Seminar	0
13e	Course contact hours - Studio	0
13f	Course contact hours - Online	0
13g	Course contact hours - Colloquium	0
13h	Course contact hours – Field Experience	0
13j	Course contact hours - Internship	0
13k	Course contact hours - Research	0
13m	Course contact hours - Workshop	0
13n	Course contact hours – Other (specify)	0
14	Instructor Workload	3
15	Grade Mode (check all appropriate):	☒ Standard ☐ Credit/No Credit (undergrad only)
16	Maximum Enrollment	40
17	Catalog Description (50 words or less)	This course covers fundamental concepts of digital electronics, including binary numbers, binary mathematical operations, error codes, Boolean algebra, Boolean logic simplification,

		Karnaugh maps, gate-level schematics, combinational logic, sequential logic, latches, flip-flops, counters, clocks, registers, and memory. Prerequisites: a grade of C or better in both ENGR/PHYS 135 and ENGR/PHYS 136.
18	List any prerequisites (course/s, test scores, class standing, major, etc.)	A grade of C or better in both ENGR/PHYS 135 and ENGR/PHYS 136 (Intro. to Electric Circuits/Lab)
19	List any co-requisites	ENGR 211 (Digital Logic Lab)
20	If the course is cross listed, specify the Course ID(s) MUST have signature in Section V off all Department Chairs	No
21	Does this course have any special student costs? Yes/No <i>Attach a <u>completed</u> "Request to Add or Change Course Fees" form. Describe in section IV-F.</i>	No
22a	How often will it be offered? Fall	Offered
22b	How often will it be offered? Spring	As needed
22c	How often will it be offered? Summer	As needed
23	Frequency? All, odd, or even years?	All
24	First semester this course will be offered <i>New courses may not be taught under their approved course ID until they appear in the catalog, therefore, no earlier than the coming fall.</i>	Fall 2015
25	List course/s that should be deleted, include the last semester for course/s being deleted <i>A course cannot be deleted when it is a requirement in a degree plan in any department.</i>	None
26	Is this course required for a degree/s? Yes/No <i>If "Yes," attach a revised degree plan(s) reflecting the placement of the new course.</i>	This course is an elective option for students interested in electrical engineering topics. All engineering students must select 14 hours of engineering electives to complete their degree.
27	Should the addition of this course be retroactively applied to the degree plan it supports? Yes/no and explain	No
28	Has this course been offered as a Special Topics course? Yes/No <i>If "Yes," specify the Course ID and enrollment for each term it was taught. (Note: New courses are NOT required to be taught as Special Topics courses prior to being approved. However, courses may be taught once as Special Topics before it is proposed. See the Course Descriptions section of the Catalog for details.)</i>	Yes. ENGR 240 – Fall 2013, Enrollment 8; ENGR 240 – Fall 2014, Enrollment 18.

29	List any courses in which content overlaps the proposed course. (Course ID and Name) <i>Attach statement from instructor/dept chair of existing course justifying the new offering in Section IV-D.</i>	None
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II. Curriculum

1	Degree Plan Explanation of how this course affects degree requirements.	This course fits in the menu of elective courses from which BSE students are required to take 14 hours.
2	Justification State the justification for adding this course to the current curriculum. Represent the need.	The course provides an in-depth introduction to digital electronic circuits, which are ubiquitous in society. Students enrolled in this course will gain the fundamental knowledge required to understand and begin to design digital electronic devices. This course will act as a prerequisite for more advanced elective courses pertaining to embedded systems and digital design. Additionally, the class will aid in meeting ABET requirements.

III. Course Design

1. Audience and Course Goal

1	Describe the intended audience, including prerequisite skills.	BSE students with an interest in circuit design are strongly encouraged to enroll in this course as one of their electives. This course provides a foundation upon which to build marketable skills in digital circuit design.
2	State the overarching course goal(s) in performance terms.	In this course students will learn how to interpret digital circuit schematics, simplify digital circuits using Boolean algebra and Karnaugh maps, and design simple digital circuits to perform specific functions.

2. Competencies and Measurements

ABET Learning Objectives	Course Competencies	Measurement
3(a) an ability to apply knowledge of mathematics, science, and engineering	Perform decimal to binary conversion and binary arithmetic with signed and unsigned binary numbers	Homework, Test 1, Final Exam
	Identify and interpret hexadecimal numbers in decimal format	Homework, Test 1, Final Exam
	Discuss error detection in transmitted binary information	Homework, Test 1, Final Exam
	Describe the functions of basic logic gates (NOT, AND, OR, NAND, NOR, XOR)	Homework, Test 1, Final Exam
	Analyze and simplify combinational logic circuits using Boolean algebra	Homework, Test 1, Final Exam
	Employ a Karnaugh map to simplify Boolean expressions	Homework, Test 1, Final Exam
	Explain the functions of adders, comparators, encoders/decoders, and multiplexers	Homework, Test 2, Final Exam
	Discuss the differences between latches and flip-flops	Test 2, Final Exam
	Relate the differences between registers and counters	Homework, Test 2, Final Exam
3(e) an ability to identify, formulate, and solve engineering problems	Design combinational logic circuits to perform functions expressed in Boolean algebra	Design Homework, Tests 1 and 2, Final Exam
	Construct basic latches using logic gates	Homework, Test 2, Final Exam
	Design frequency dividers and counters using flip-flops	Design Homework, Test 2, Final Exam

3. Text and Resources

1. Give the full publication information of the textbook/s and other required resources and outside readings.

Text: *Digital Fundamentals*, by Thomas L. Floyd, 11th Edition, Prentice Hall, 2014, ISBN 978-0-13273-796-8

2. For **combined undergraduate/graduate** courses, make two lists:
 - a. full publication information; label **Undergraduate**.
 - b. full publication information; label **Graduate**. Indicate number of pages required.

IV. Supporting Documentation

Supporting documents must accompany proposal prior to preliminary approval by chair and dean.

1	Library — Submit new course application and syllabus to the Dean of the Library. Consult with the director and establish a deadline for completion of the library report. Attach the signed copy of the Library report.
2	Instructional Design — The application and syllabus must be reviewed by the Adams Center. Attach a copy of the Adams Center Review Letter.
3	Content Overlap — Include one document for each course you listed in Section I-I. Attach statement from instructor/dept chair of existing course justifying the new offering.
4	Departmental Resources — List the resources that support the course and are available only through the department, if applicable. Attach the list of the holdings and the location/s.
5	Resources — List resources (other than library or departmental resources) that are needed to support this course (computers, lab equipment, other technology, etc.). Attach a complete list of all items and indicate possible sources or estimated cost of each. List the sources of any needed funds.
6	Expenses — List additional expenses needed to implement this course (full-time or part-time faculty, graduate or lab assistants, student employees, travel, special student costs, room renovation, storage facility, etc.). Attach a complete list of all items, the estimated cost of each and the source of the funds.
7	Justification — Attach all documents referred to in Section II-2
8	Syllabus — Attach the syllabus for the course based upon the anticipated first-semester offering.
9	Provide documentation for all additional attachments here

V. Preliminary Approvals

All supporting documentation has been assembled and attached to this application. I believe this course is ready to be presented to appropriate councils. We have a plan to fund this new course.

Course Number and Name ENGR 210 Digital Logic

Primary Department

James T. Dondy
Department Chair
4/21/2015
Date

[Signature]
Dean of the College
4/23/15
Date

Cross-listing Department *Add more lines if multiple departments are cross-listing*

Department Chair

Date

Dean of the College

Date

Cross-listing Department *Add more lines if multiple departments are cross-listing*

Department Chair

Date

Dean of the College

Date

VI. Actions *Place all actions on one page.*

Course ID: ENGR 210

Course Title: Digital Logic

1. College Academic Council: (for all courses)

Note: Insert additional lines for College Academic Council action for each college involved in cross-listed courses.

Approved ☒ Denied ☐ Lynette Long Ryan 4-23-15
College Dean or Director Date

Approved ☐ Denied ☐ _____
College Dean or Director Date

2. Graduate Council: (for graduate level courses)

Approved ☐ Denied ☐ _____
Dean of Graduate School Date

3. Teacher Education Council: (when applicable)

Approved ☐ Denied ☐ _____
TEC Chair Date

4. University General Education Council: (when applicable)

Approved ☐ Denied ☐ _____
Provost's designee Date

5. University Undergraduate Academic Council: (*undergraduate level courses*)

Approved ☐ Denied ☐ _____
Vice Provost Date

6. Academic Provost: (for all courses)

Approved ☐ Denied ☐ _____
Provost Date

7. President of the University: (for all courses)

Approved ☐ Denied ☐ _____
President Date

Attach notes, comments, or conditions from appropriate councils: